

GPIO Mapping - 40-way connector

Pull	ALT0	ALT1	ALT3	ALT4	ALT5	Function/GPIO	J8 Pin	Function/GPIO	Pull	ALT0	ALT1	ALT3	ALT4	ALT5	
						3.3V	1	2	5.0V						
High	SDA1	SA3				GPIO2	3	4	5.0V						
High	SCL1	SA2				GPIO3	5	6	0V						
High	GPCLK0	SA1				GPIO4	7	8	GPIO14	Low	TXD0	SD6		TXD1	
						0V	9	10	GPIO15	Low	RXD0	SD7		RXD1	
Low		SD9	RTS0	SPI1_CE1_N	RTS1	GPIO17	11	12	GPIO18	Low	PCM_CLK	SD10	BSCSL	SPI1_CE0_N	PWM0
Low			SD1_I_ARM_TMS			GPIO27	13	14	0V						
Low		SD14	SD1_I_ARM_TRST			GPIO22	15	16	GPIO23	Low		SD15	SD1_I_ARM_RTCK		
						3.3V	17	18	GPIO24	Low		SD16	SD1_I_ARM_TDO		
Low	SPIO_MOSI	SD2				GPIO10	19	20	0V						
Low	SPIO_MISO	SD1				GPIO9	21	22	GPIO25	Low		SD17	SD1_I_ARM_TCK		
Low	SPIO_SCLK	SD3				GPIO11	23	24	GPIO8	High	SPI_CE0_N	SD0			
						0V	25	26	GPIO7	High	SPI_CE1_N	SWE_N/SRW_N			
High	SDA0	SA5				(GPIO0) ID_SD	27	28	ID_SC (GPIO1)	High	SCL0	SA4			
High	GPCLK1	SA0				GPIO5	29	30	0V						
High	GPCLK2	SOE_N/SE				GPIO6	31	32	GPIO12	Low	PWM0	SD4		ARM_TMS	
Low	PWM1	SD5				GPIO13	33	34	0V						
Low	PCM_FS	SD11	BSCSL	SPI1_MISO	PWM1	GPIO19	35	36	GPIO16	Low		SD8	CTS0	SPI1_CE2_N	CTS1
Low			SD1_I_ARM_TDI			GPIO26	37	38	GPIO20	Low	PCM_DIN	SD12	BSCSL	SPI1_MOSI	GPCLK0
						0V	39	40	GPIO21	Low	PCM_DOU	SD13	BSCSL	SPI1_SCLK	GPCLK1

Notes:

1. Pins 1-26 are identical to Raspberry Pi Model B / Model A revision 2.0
2. Connector is a double-row, 40-way pin header (an expansion of Model B / Model A 26-way)

3. ID_SC and ID_SD pins.

These pins are reserved for ID EEPROM.

At boot time this I2C interface will be interrogated to look for an EEPROM that identifies the attached board and allows automatic setup of the GPIOs (and optionally, Linux drivers).

DO NOT USE these pins for anything other than attaching an I2C ID EEPROM. Leave unconnected if ID EEPROM not required.